SECRET

Approved For Release 2004/05/05 : CIA-RDP78B05171A000600010106-6

NPIC/TSSG/RED/SDB-005-70 16 January 1970

MEMORA	NDUM	FOR	THE	RECORD
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SUBJECT: High Precision Stereo Comparator Program

ĺ	1. A meeting of the Stereo Comparator Coordinating Group was held on 12 January 1970 in the 5S-453 Conference Room. In attendance were:
L	2. The following items were discussed at the meeting:
	a. The program schedule calls for 82% of the work to be completed and 77% is actually complete.
	b. will be the technical monitor on this program for the period 19 January to 2 March 1970.
	c. Logistics is proceeding with plans to air transport the optical system from late in April 1970.
	project Manager monitored the optical subcontract at from 5-17 December. He reports that the mechanical problem
	areas have been resolved. He will return to the 22 January 1976 to work up the acceptance test plan with Latest information from indicates that the optical system will be ready for to start acceptance tests on 25 February.
	menting the Spare Parts List as soon as possible. The lasers went down and the interferometer work was held up while the lasers were sent back to the manufacturer for recharging had an electronic part fail but fortunately had a spare on hand, and reports that two potentiometers have failed but there is only one replacement on hand.
	f. has reported that the present 16K memory core of the Honeywell 516 Computer will not be adequate to handle the entire
	computer program. is investigating the various approaches to solving this problem and NPIC personnel are also checking out. The alternatives at this time are:
	(1) Reuse memory areas, thereby destroying the program and requiring the reloading of the computer after each stereo setup.

Declass Review By NIMA Declase 2004/05/05 : CIA-RD 78B 05171A000600010106-6

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	(2) Provide a memory drum to store some of the program.
	(3) Provide added core memory to the Honeywell 516 Computer.
X1	will visit on 19 and
, , ,	20 January 1970 to review the computer program and arrive at a
	recommended approach to the problem. will then submit by 2 February a proposal for a change-in-scope to provide the added
	memory.
X1	h. pf TSSG/SSD will visit during the week of 18 January 1970 to coordinate the site preparation work
X1	with personnel.
	i. has submitted a test plan for the correlation system
X1	A a nowtions of the test blane boom
X1	- The week of 12 delically to come of
	a common understanding on the acceptance test for this subsystem.
	3. Attached is an updated PERT chart based on the best estimates
X1	can give at this time.
	Froject Monitor
	npic/tssg/red/sdb
	Attachment: PERT Chart
	Distribution: Original - Route & File
X1	1 - MPIC/TSSG/RED
^ I	2 - MPIC/IEG/PHD (
X1	2 - NPIC/PSG/AID (1 - NPIB/PPBS
Λ1	1 ~ MPIC/TSSG/APS4
	1 - NPIC/TSSG/SSD
	2 - NPIC/TSSG/ESD 2 - NPIC/TSSG/RED
	2 - NPIC/TSSG/RED/SDB